

Electronic Acknowledgement Receipt

EFS ID:	1246726
Application Number:	10699756
Confirmation Number:	4258
Title of Invention:	In-circuit configuration architecture with configuration on initialization function for embedded configurable logic array
First Named Inventor:	Albert Sun
Customer Number:	22470
Filer:	Mark Andrew Haynes/Paula Hurley
Filer Authorized By:	Mark Andrew Haynes
Attorney Docket Number:	MXIC 1521-1
Receipt Date:	11-OCT-2006
Filing Date:	03-NOV-2003
Time Stamp:	12:11:40
Application Type:	Utility
International Application Number:	

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Request for Corrected Filing Receipt	efsrequestfr20061011.pdf	118137	no	3

Warnings:**Information:****Total Files Size (in bytes):**

118137

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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.